

FOR MESSRS : _____

DATE : Jan.15th,2025

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

TX27D200VM0AVB

Contents


No.	ITEM	SHEET No.	PAGE
1	COVER	7B64PS 2701-TX27D200VM0AVB-5	1-1/1
2	RECORD OF REVISION	7B64PS 2702-TX27D200VM0AVB-5	2-1/2~2/2
3	GENERAL DATA	7B64PS 2703-TX27D200VM0AVB-5	3-1/1
4	ABSOLUTE MAXIMUM RATINGS	7B64PS 2704-TX27D200VM0AVB-5	4-1/1
5	ELECTRICAL CHARACTERISTICS	7B64PS 2705-TX27D200VM0AVB-5	5-1/2~2/2
6	OPTICAL CHARACTERISTICS	7B64PS 2706-TX27D200VM0AVB-5	6-1/2~2/2
7	BLOCK DIAGRAM	7B64PS 2707-TX27D200VM0AVB-5	7-1/1
8	RELIABILITY TESTS	7B64PS 2708-TX27D200VM0AVB-5	8-1/1
9	LCD INTERFACE	7B64PS 2709-TX27D200VM0AVB-5	9-1/10~10/10
10	OUTLINE DIMENSIONS	7B64PS 2710-TX27D200VM0AVB-5	10-1/2~2/2
11	TOUCH PANEL	7B64PS 2711-TX27D200VM0AVB-5	11-1/1
12	APPEARANCE STANDARD	7B64PS 2712-TX27D200VM0AVB-5	12-1/5~5/5
13	PRECAUTIONS	7B64PS 2713-TX27D200VM0AVB-5	13-1/2~2/2
14	DESIGNATION OF LOT MARK	7B64PS 2714-TX27D200VM0AVB-5	14-1/1

ACCEPTED BY: _____

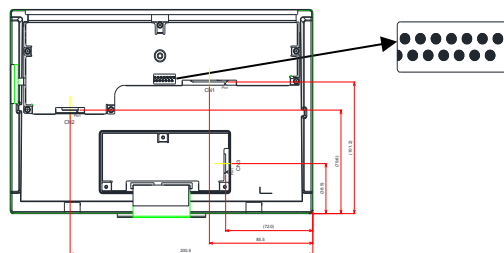
PROPOSED BY: _____

Alex Lee

2. RECORD OF REVISION

DATE	SHEET No.	SUMMARY																																																		
Dec.13,'21	7B64PS 2711- TX27D200VM0AVB-2 Page 11-1/1	11.3 CONTROLLER CHARACTERISTICS Added: Firmware version : A0651E_0001_PCT-106F-03_EE80W462840-CBBC-B001_v03_ECN_20211129																																																		
	7B64PS 2712- TX27D200VM0AVB-2 Page 12-3/5	12.2 LCD APPEARANCE SPECIFICATION Revised : Note 1 For bright dot-defect, showing black pattern, visible with ND 5% filter size over 1/2 dot is defined. <div>↓</div> For bright dot-defect, showing black pattern, visible with defect size over 1/2 dot is defined.																																																		
Dec.24,'21	7B64PS 2712- TX27D200VM0AVB-3 Page 12-4/5	12.3 TOUCH PANEL APPEARANCE SPECIFICATION Revised : Maximum number of Scratches																																																		
		<table><tr><th>Item</th><th colspan="4">Criteria</th><th>Applied zone</th></tr><tr><td rowspan="4">Scratches</td><td>Width (mm)</td><td>Length (mm)</td><td>Maximum number</td><td>Minimum Space</td><td rowspan="4">A</td></tr><tr><td>$W \leq 0.1$</td><td>$L \leq 5$</td><td>Ignored</td><td>-</td></tr><tr><td>$0.1 < W \leq 0.15$</td><td>$L \leq 5$</td><td>3</td><td>5mm</td></tr><tr><td>$0.15 < W$</td><td>-</td><td>0</td><td></td></tr></table> <div>↓</div> <table><tr><th>Item</th><th colspan="4">Criteria</th><th>Applied zone</th></tr><tr><td rowspan="4">Scratches</td><td>Width (mm)</td><td>Length (mm)</td><td>Maximum number</td><td>Minimum Space</td><td rowspan="4">A</td></tr><tr><td>$W \leq 0.1$</td><td>$L \leq 5$</td><td>Ignored</td><td>-</td></tr><tr><td>$0.1 < W \leq 0.15$</td><td>$L \leq 5$</td><td>5</td><td>5mm</td></tr><tr><td>$0.15 < W$</td><td>-</td><td>0</td><td></td></tr></table>					Item	Criteria				Applied zone	Scratches	Width (mm)	Length (mm)	Maximum number	Minimum Space	A	$W \leq 0.1$	$L \leq 5$	Ignored	-	$0.1 < W \leq 0.15$	$L \leq 5$	3	5mm	$0.15 < W$	-	0		Item	Criteria				Applied zone	Scratches	Width (mm)	Length (mm)	Maximum number	Minimum Space	A	$W \leq 0.1$	$L \leq 5$	Ignored	-	$0.1 < W \leq 0.15$	$L \leq 5$	5	5mm	$0.15 < W$	-
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Jan.03,'23	7B64PS 2701- TX27D200VM0AVB-4 Page 1-1/1	Company logo changed : <div><div>KOE JDI Taiwan Inc.</div><div>→</div><div> Japan Display Inc.</div></div>																																																		
	7B64PS 2714- TX27D200VM0AVB-4 Page 14-1/1																																																			
Jan.15,'25	7B64PS 2703- TX27D200VM0AVB -5 PAGE 3-1/1	3.1 DISPLAY FEATURES amorphous silicon TFT → LTPS TFT Power Consumption: 1.16W → 0.264W																																																		
	7B64PS 2705- TX27D200VM0AVB -5 PAGE 5-1/2	5.1 LCD CHARACTERISTICS <table><tr><th>Item</th><th>Min.</th><th>Typ.</th><th>Max.</th><th>Unit</th></tr><tr><td>Power Supply Current</td><td>-</td><td>350</td><td>440</td><td>mA</td></tr></table> <div>↓</div> <table><tr><th>Item</th><th>Min.</th><th>Typ.</th><th>Max.</th><th>Unit</th></tr><tr><td>Power Supply Current</td><td>-</td><td>80</td><td>130</td><td>mA</td></tr></table>					Item	Min.	Typ.	Max.	Unit	Power Supply Current	-	350	440	mA	Item	Min.	Typ.	Max.	Unit	Power Supply Current	-	80	130	mA																										
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2. RECORD OF REVISION

DATE	SHEET No.	SUMMARY																				
Jan.15,'25	7B64PS 2706- TX27D200VM0AVB -5 PAGE 6-1/2	6. OPTICAL CHARACTERISTICS <table><tr><th>Item</th><th>Min.</th><th>Typ.</th><th>Max.</th><th>Unit</th></tr><tr><td>Brightness of White</td><td>640</td><td>800</td><td>-</td><td>cd/m²</td></tr></table> <div>↓</div> <table><tr><th>Item</th><th>Min.</th><th>Typ.</th><th>Max.</th><th>Unit</th></tr><tr><td>Brightness of White</td><td>800</td><td>1000</td><td>-</td><td>cd/m²</td></tr></table>	Item	Min.	Typ.	Max.	Unit	Brightness of White	640	800	-	cd/m ²	Item	Min.	Typ.	Max.	Unit	Brightness of White	800	1000	-	cd/m ²
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Brightness of White	800	1000	-	cd/m ²																		
7B64PS 2707- TX27D200VM0AVB -5 PAGE 7-1/1	7. BLOCK DIAGRAM Revised the block diagram.																					
7B64PS 2709- TX27D200VM0AVB -5 PAGE 9-6/10	9.5 TIME TABLE <table><tr><th colspan="2">Item</th><th>Min.</th></tr><tr><td>Horizontal</td><td>Cycle Time</td><td>1316</td></tr><tr><td>Vertical</td><td>Cycle Time</td><td>771</td></tr></table> → <table><tr><th colspan="2">Item</th><th>Min.</th></tr><tr><td>Horizontal</td><td>Cycle Time</td><td>1322</td></tr><tr><td>Vertical</td><td>Cycle Time</td><td>778</td></tr></table>	Item		Min.	Horizontal	Cycle Time	1316	Vertical	Cycle Time	771	Item		Min.	Horizontal	Cycle Time	1322	Vertical	Cycle Time	778			
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7B64PS 2710- TX27D200VM0AVB -5 PAGE 10-2/2	10.2 REAR VIEW Added probes from 9 to 19 pins. 																					
7B64PS 2714- TX27D200VM0AVB -5 PAGE 14-1/1	14. DESIGNATION of LOT MARK Added: <table><tr><th>REV. No</th><th>ITEM</th><th>REMARKS</th></tr><tr><td>A</td><td>-</td><td>-</td></tr><tr><td>B</td><td>LCD source change</td><td>PCN 1110</td></tr></table>	REV. No	ITEM	REMARKS	A	-	-	B	LCD source change	PCN 1110												
REV. No	ITEM	REMARKS																				
A	-	-																				
B	LCD source change	PCN 1110																				

3. GENERAL DATA

3.1 DISPLAY FEATURES

This module is a 10.6" WXGA of 16:9 format of LTPS TFT. The pixel format is vertical stripe and sub pixels are arranged as R(red), G(green), B(blue) sequentially. This display is RoHS compliant, and COG (chip on glass) technology and LED backlight are applied on this display.

Part Name	TX27D200VM0AVB
Module Dimensions	250.0 (W) mm × 157.0 (H) mm × 14.4 (D) mm (Typ.)
LCD Active Area	231.36 (W)mm x 138.816(H)mm
Pixel Pitch	0.18075 (W) mm × 0.18075 (H) mm
Resolution	1280× 3 (RGB) (W) × 768 (H) dots
Color Pixel Arrangement	RGB Vertical Stripe
LCD Type	Transmissive Type, Normally Black
Display Type	Active Matrix
Number of Colors	262K (6-bit RGB) / 16.7M (8-bit RGB) Colors
Backlight	Light Emitting Diode (LED)
Weight	680g (typ)
Interface	LVDS ; 20pins
Power Supply Voltage	3.3V for LCD ; 12V for Backlight
Power Consumption	0.264W for LCD; 8.28W for Backlight
Viewing Direction	Super Wide Version (In-Plane Switching)
Touch Panel	Projected Capacitive type ; G-G structure
Touch Panel Interface	USB

4. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remarks
Supply Voltage	V _{DD}	-0.3	4.0	V	-
Input Voltage of Logic	V _I	-0.3	4.0	V	Note 1
Operating Temperature	T _{op}	-30	80	°C	Note 2
Storage Temperature	T _{st}	-30	80	°C	Note 2
Backlight Input Voltage	V _{LED}	-	13.2	V	-
Backlight Voltage for PWM	V _{PWM}	-0.3	14	V	-
Backlight Voltage for EN	V _{EN}	-0.3	14	V	-

Note 1: The rating is defined for the signal voltage of the interface such as CLK and pixel data pairs.

Note 2: The maximum rating is defined as above based on the chamber temperature, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:

- Background color, contrast and response time would be different in temperatures other than 25 °C.
- Operating under high temperature will shorten LED lifetime.

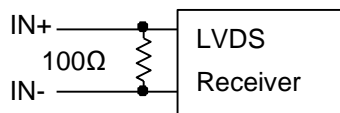
5. ELECTRICAL CHARACTERISTICS

5.1 LCD CHARACTERISTICS

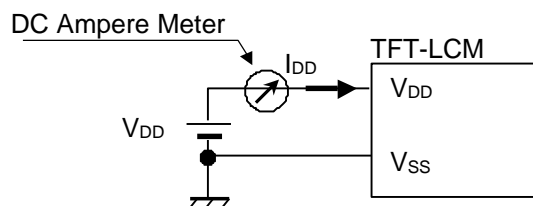
$T_a = 25\text{ }^{\circ}\text{C}$, $V_{SS} = 0\text{V}$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-	3.0	3.3	3.6	V	-
Differential Input Voltage for LVDS Receiver Threshold	V_I	V_{IH}	-	-	+100	mV	Note 1
		V_{IL}	-100	-	-		
Power Supply Current	I_{DD}	$V_{DD}-V_{SS} = 3.3\text{V}$	-	80	130	mA	Note 2,3
Frame Frequency	f_{Frame}	-	55	60	75	Hz	Note 4
CLK Frequency	f_{CLK}	-	50	68.3	80	MHz	
Logic Input Voltage	High	V_{IH}	$0.8 \times V_{DD}$	-	V_{DD}	V	MODE,SD
	Low	V_{IL}	0	-	$0.2 \times V_{DD}$	V	MODE,SD

Note 1: VCM 1.2V is common mode voltage of LVDS transmitter and receiver. The input terminal of LVDS transmitter is terminated with 100Ω .



Note 2: An all white check pattern is used when measuring I_{DD} . f_{Frame} is set to 60Hz.



Note 3: 2A fuse is applied in the module for I_{DD} . For display activation and protection purpose, power supply is recommended larger than 5A to start the display and break fuse once any short circuit occurred.

Note 4: For LVDS transmitter input.

5.2 BACKLIGHT CHARACTERISTICS

$T_a = 25\text{ }^{\circ}\text{C}$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
LED Input Voltage	V_{LED}	Backlight Unit	10.8	12	13.2	V	Note 1
LED Forward Current (Dim Control)	I_{LED}	100% duty	-	690	860	mA	Note 2
		0% duty	-	6.0	-		
Backlight Enable	BLEN	Backlight Unit	1.5	-	5.5	V	
LED Lifetime	-	$I_{LED}=690\text{mA}$	-	100K	-	hrs	Note 3
PWM signal	BL_PWM	Backlight Unit	1.5	-	5.5	V	

Note 1: Fig. 5.1 shows the LED backlight circuit.

Note 2: Dimming function can be obtained by applying PWM signal from the display interface CN2. The recommended PWM signal is 1K ~ 10KHz with 3.3 V amplitude.

Note 3: The estimated lifetime is specified as the time to reduce 50% brightness by applying 690 mA at $25\text{ }^{\circ}\text{C}$.

Note 4: 3A fuse is applied in the module for I_{LED} . For display activation and protection purpose, power supply is recommended larger than 7.5A to start the display and break fuse once any short circuit occurred

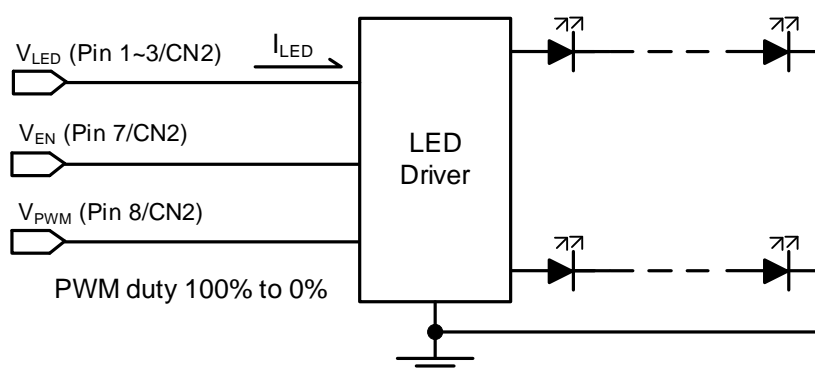


Fig 5.1

6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The ambient temperature is 25°C.
- In the dark room less than 100 lx, the equipment has been set for the measurements as shown in Fig 6.1.

$$T_a = 25^\circ\text{C}, f_{\text{Frame}} = 60\text{ Hz}, V_{\text{DD}} = 3.3\text{V}$$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Brightness of White	-	$I_{\text{LED}} = 690\text{mA}$ $\phi = 0^\circ, \theta = 0^\circ$	800	1000	-	cd/m ²	Note 1
Brightness Uniformity	-		70	-	-	%	Note 2
Contrast Ratio	CR		650	1000	-	-	Note 3
Response Time	Tr + Tf	$\phi = 0^\circ, \theta = 0^\circ$	-	24	-	ms	Note 4
NTSC Ratio	-	$\phi = 0^\circ, \theta = 0^\circ$	-	50	-	%	-
Viewing Angle	θ_x	$\phi = 0^\circ, \text{CR} \geq 10$	-	85	-	Degree	Note 5
	$\theta_{x'}$	$\phi = 180^\circ, \text{CR} \geq 10$	-	85	-		
	θ_y	$\phi = 90^\circ, \text{CR} \geq 10$	-	85	-		
	$\theta_{y'}$	$\phi = 270^\circ, \text{CR} \geq 10$	-	85	-		
Color Chromaticity	Red	X	0.54	0.59	0.64	-	Note 6
		Y	0.27	0.32	0.37		
	Green	X	0.28	0.33	0.38		
		Y	0.52	0.57	0.62		
	Blue	X	0.10	0.15	0.20		
		Y	0.07	0.12	0.17		
	White	X	0.26	0.31	0.36		
		Y	0.27	0.32	0.37		

Note 1: The brightness is measured from the center point of the panel, P5 in Fig. 6.2, for the typical value.

Note 2: The brightness uniformity is calculated by the equation as below:

$$\text{Brightness uniformity} = \frac{\text{Min. Brightness}}{\text{Max. Brightness}} \times 100\%$$

which is based on the brightness values of the 9 points measured by BM-5 as shown in Fig. 6.2.

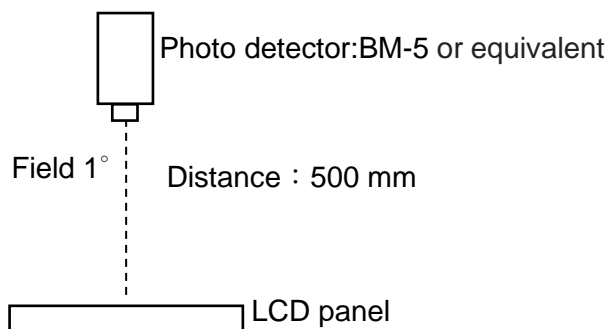


Fig. 6.1

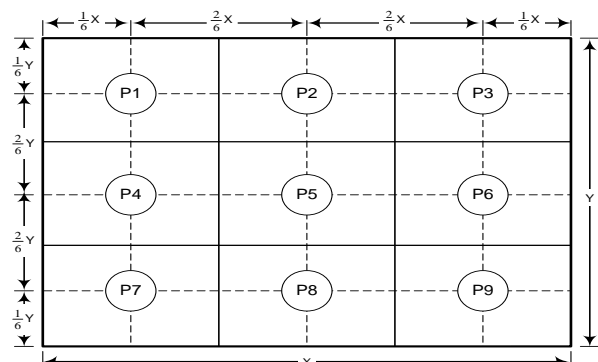


Fig. 6.2

Note 3: The Contrast ratio is measured from the center point of the panel, P5, and defined as the following equation:

$$CR = \frac{\text{Brightness of White}}{\text{Brightness of Black}}$$

Note 4: The definition of response time is shown in Fig. 6.3. The rising time is the period from 10% brightness to 90% brightness when the data is from black to white. Oppositely, Falling time is the period from 90% brightness rising to 10% brightness.

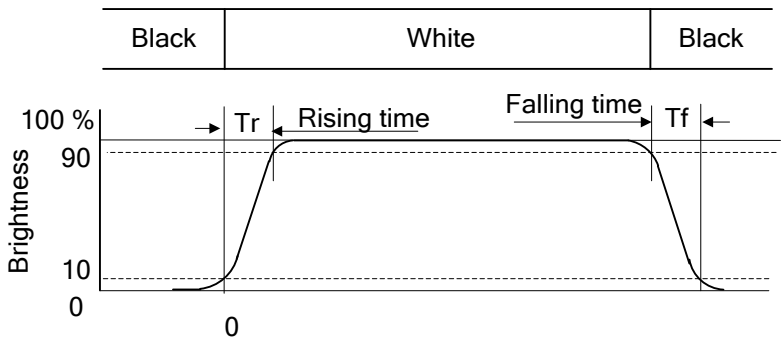


Fig. 6.3

Note 5: The definition of viewing angle is shown in Fig. 6.4. Angle ϕ is used to represent viewing directions, for instance, $\phi = 270^\circ$ means 6 o'clock, and $\phi = 0^\circ$ means 3 o'clock. Moreover, angle θ is used to represent viewing angles from axis Z toward plane XY.

The display is super wide viewing angle version, so that the best optical performance can be obtained from every viewing direction.

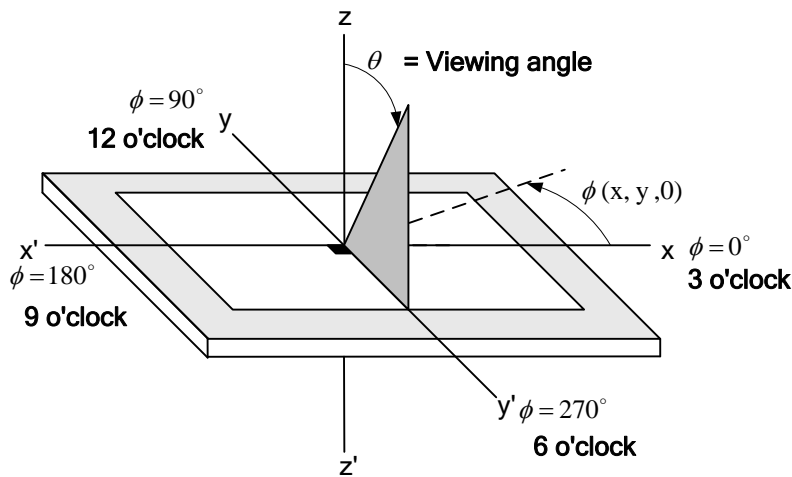
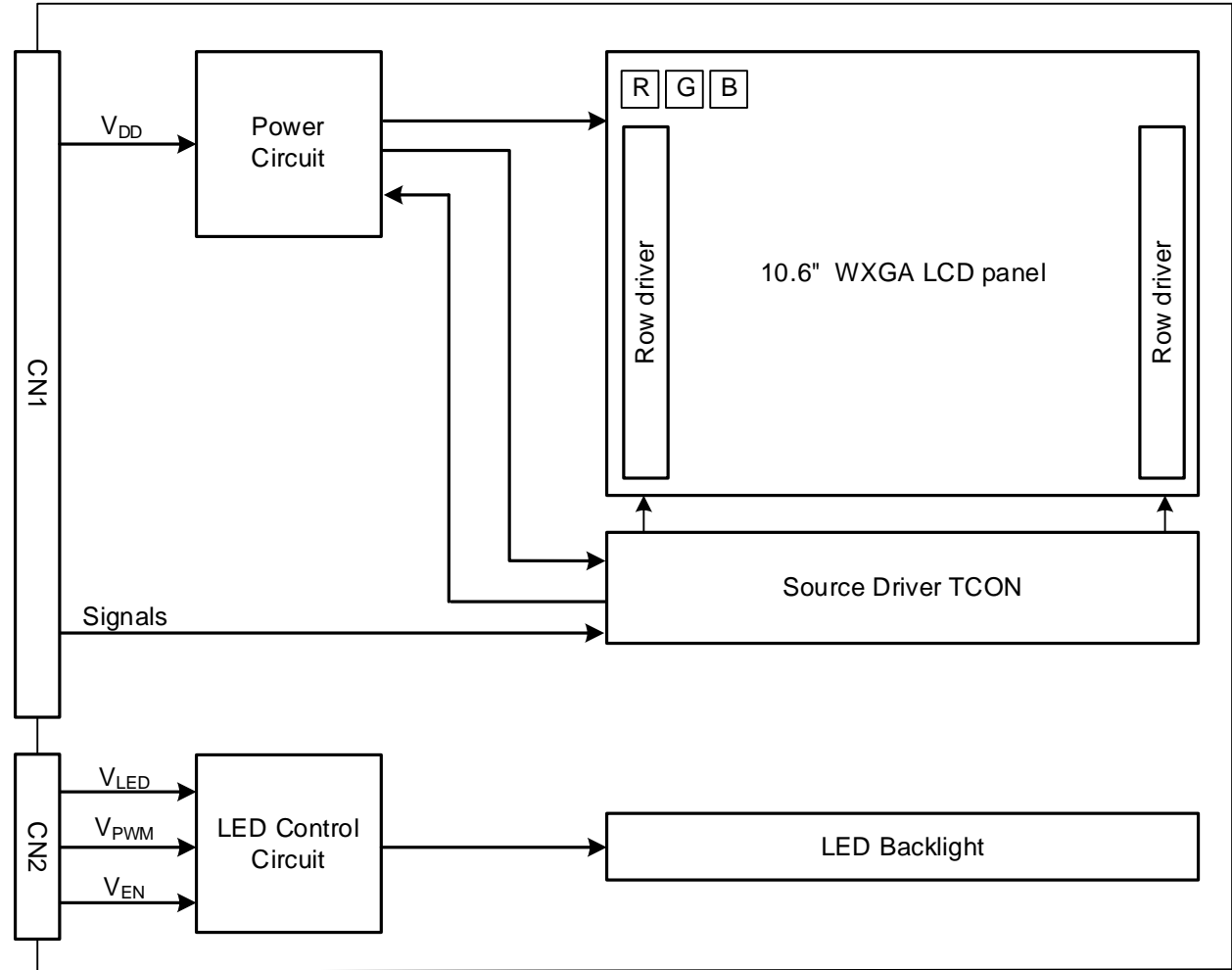


Fig. 6.4

Note 6: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig. 6.2.

7. BLOCK DIAGRAM



Note 1: Signals are SD, MODE, CLK and pixel data pairs.

8. RELIABILITY TESTS

Test Item	Condition	
High Temperature	1) Operating 2) 80 °C	240 hrs
Low Temperature	1) Operating 2) -30 °C	240 hrs
High Temperature	1) Storage 2) 80 °C	240 hrs
Low Temperature	1) Storage 2) -30 °C	240 hrs
Heat Cycle	1) Operating 2) -30 °C ~80 °C 3) 3hrs~1hr~3hrs	240 hrs
Thermal Shock	1) Non-Operating 2) -30 °C ↔ 80 °C 3) 0.5 hr ↔ 0.5 hr	240 hrs
High Temperature & Humidity	1) Operating 2) 65 °C & 85%RH 3) Without condensation	240 hrs (Note 3)
Vibration	1) Non-Operating 2) 10~200 Hz 3) 5G 4) X, Y, and Z directions	1 hrs for each direction
Mechanical Shock	1) Non-Operating 2) 10 ms 3) 80G 4) ±X, ±Y and ±Z directions	Once for each direction
ESD	1) Operating 2) Tip: 150 pF, 330 Ω 3) Air discharge for glass: ± 12KV 4) Contact discharge for metal frame: ± 8KV	1) Glass: 9 points 2) Metal frame: 8 points (Note4)

Note 1: Display functionalities are inspected under the conditions defined in the specification, and there shall be no change which may affect practical display function after the reliability tests.

Note 2: The display is not guaranteed for use in corrosive gas environments.

Note 3: Under the condition of high temperature & humidity, if the temperature is higher than 65°C, the humidity needs to be reduced as Fig. 8.1 shown.

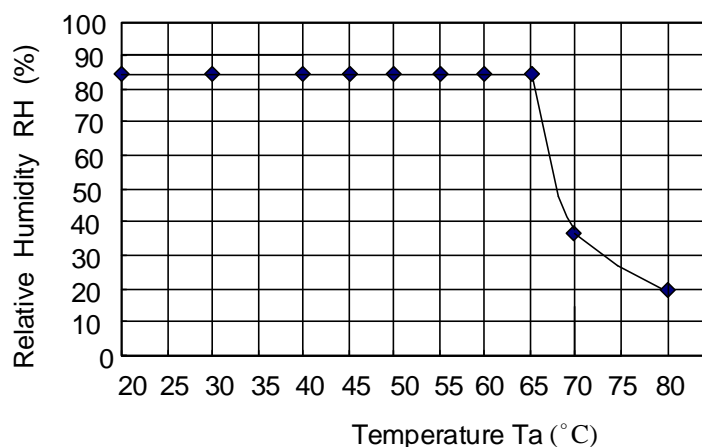


Fig. 8.1

Note 4: All pins of LCD interface (CN1) have been tested by ± 100V contact discharge of ESD under non-operating condition.

9. LCD INTERFACE

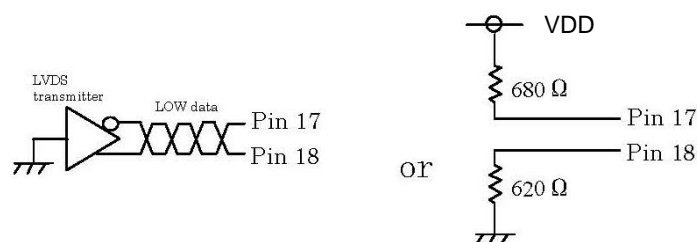
9.1 INTERFACE PIN CONNECTIONS

The display interface connector (CN1) is FI-SE20P-HFE made by JAE and pin assignment is as below:

Pin No.	Signal	Function (MODE = Low)		Function (MODE = High)
		6 bit input	8 bit input	8 bit input
1	V _{DD}	+3.3V Power Supply for Logic		+3.3V Power Supply for Logic
2	V _{DD}	+3.3V Power Supply for Logic		+3.3V Power Supply for Logic
3	V _{SS}	GND		GND
4	V _{SS}	GND		GND
5	Link 0-	R0~R5, G0	R2~R7, G2	R0~R5, G0
6	Link 0+	R0~R5, G0	R2~R7, G2	R0~R5, G0
7	V _{SS}	GND		GND
8	Link 1-	G1~G5, B0~B1	G3~G7, B2~B3	G1~G5, B0~B1
9	Link 1+	G1~G5, B0~B1	G3~G7, B2~B3	G1~G5, B0~B1
10	V _{SS}	GND		GND
11	Link 2-	B2~B5, DE	B4~B7, DE	B2~B5, DE
12	Link 2+	B2~B5, DE	B4~B7, DE	B2~B5, DE
13	V _{SS}	GND		GND
14	CLK IN-	Pixel Clock -		Pixel Clock -
15	CLK IN+	Pixel Clock +		Pixel Clock +
16	V _{SS}	GND		GND
17	Link 3-	See:*2)	R0~R1, G0~G1, B0~B1	R6~R7, G6~G7, B6~B7
18	Link 3+	See:*2)	R0~R1, G0~G1, B0~B1	R6~R7, G6~G7, B6~B7
19	MODE	Low= 6bit / 8bit		High= 8bit
20	SD	Scan direction control (Low, Default = Normal, High = Reverse)		

Note 1: Link n- and Link n+ (n=0, 1, 2, 3), CLK IN- and CLK IN+ should be wired by twist-pairs.

Note 2: Recommended wiring of Pin 17,18 (6 bit input)



The backlight connector (CN2) is FI-S8P-HFE made by JAE, and pin assignment is as below:

Pin No.	Signal	Signal
1	V _{LED}	Backlight Power Input
2	V _{LED}	Backlight Power Input
3	V _{LED}	Backlight Power Input
4	V _{SS}	GND
5	V _{SS}	GND
6	V _{SS}	GND
7	V _{EN}	Backlight Enable (High: ON / Low : OFF)
8	V _{PWM}	Backlight Dimming (Note 1)

Note 1: Normal brightness: 100% PWM duty; Brightness control: 100% to 0% PWM duty.

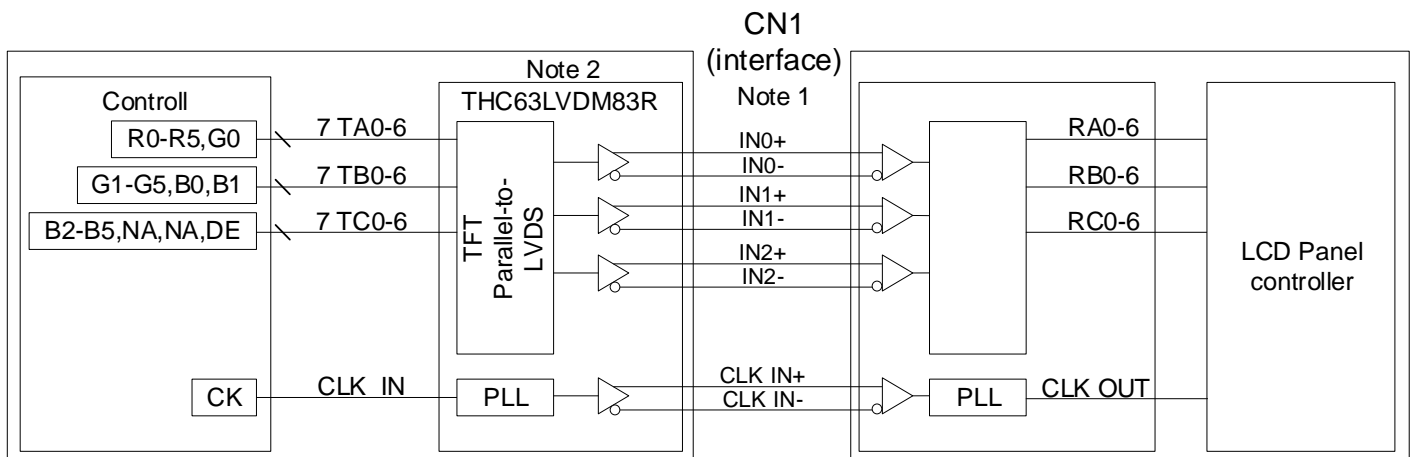
The touch panel connector (CN3) is SM12B-SHLS-TF(LF)(SN) made by JST, and pin assignment is as below:

Pin No.	Signal	Signal
1	NC	(Internal use) Note.1
2	NC	(Internal use) Note.1
3	NC	(Internal use) Note.1
4	GND	Touch panel controller GND
5	NC	(Internal use) Note.1
6	NC	(Internal use) Note.1
7	NC	(Internal use) Note.1
8	NC	(Internal use) Note.1
9	REST	Touch panel reset (H: Usually ; L : Reset)
10	D-	USB D- Terminal
11	D+	USB D+ Terminal
12	Power supply 5V	USB power supply (5V)

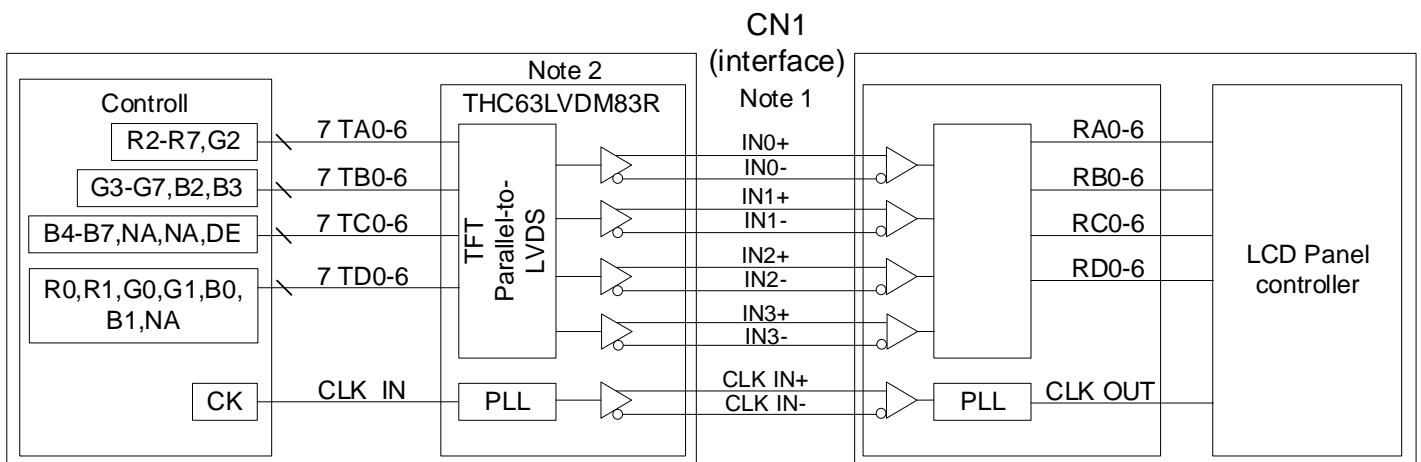
Note 1: Please don't use NC (pin1 to 3 and pin5 to 8),because they are for internal use only.

9.2 LVDS INTERFACE

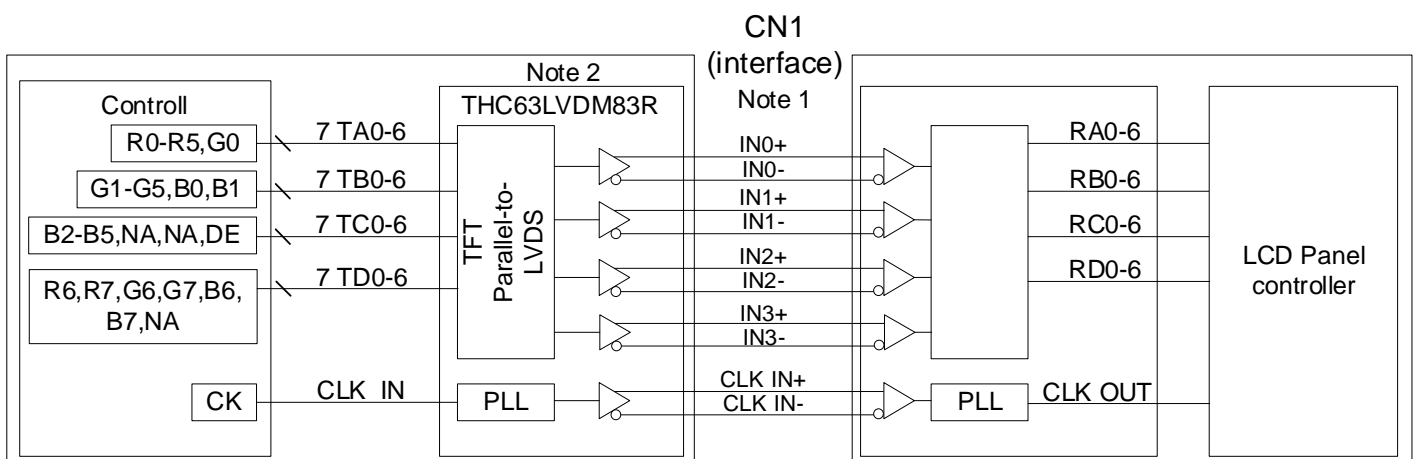
9.2.1 MODE = Low (6 bit input)



9.2.2 MODE = Low (8 bit input)



9.2.3 MODE = High (8 bit input)

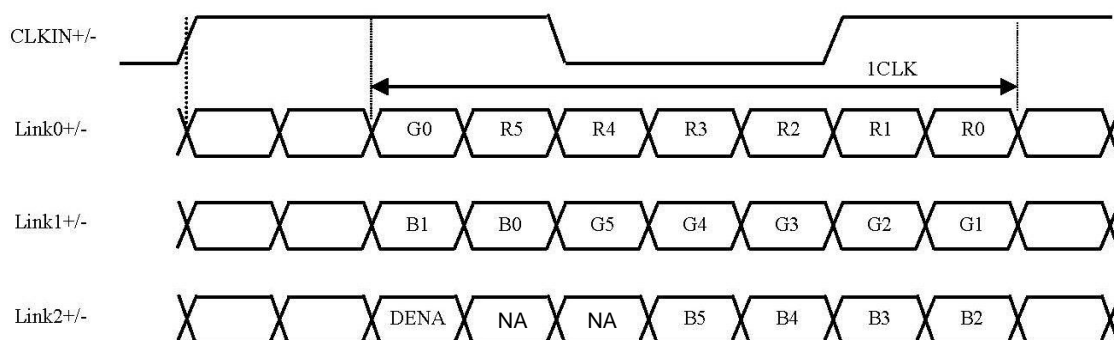


Note 1: LVDS cable impedance should be 100 ohms per signal line when each 2-lines (+, -) is used in differential mode.

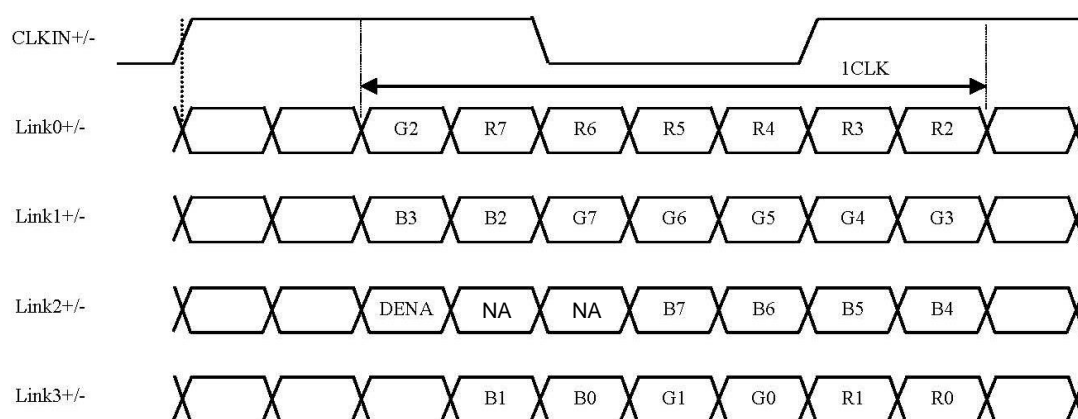
Note 2: The recommended transmitter, THC63LVDM83R, is made by Thine or equivalent, which is not contained in the module.

9.3 LVDS DATA FORMAT

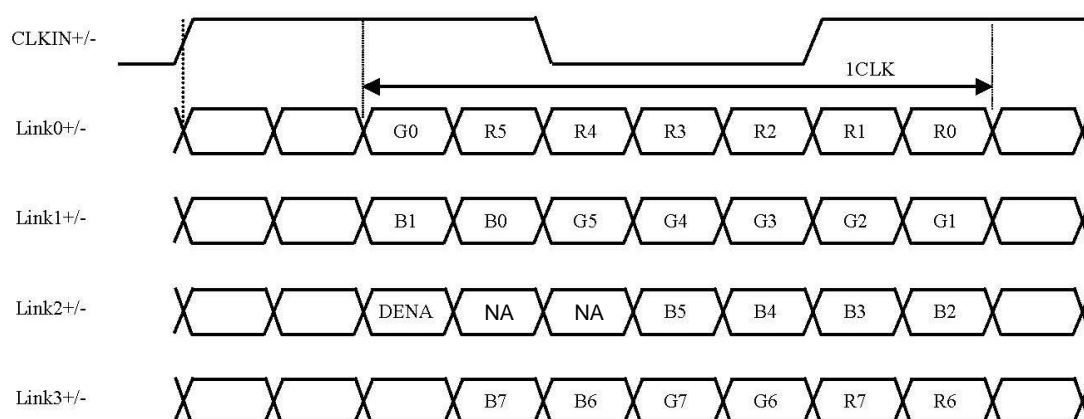
9.3.1 MODE = Low (6 bit input)



9.3.2 MODE = Low (8 bit input)



9.3.3 MODE = High (8 bit input)



9.4 TIMING CHART

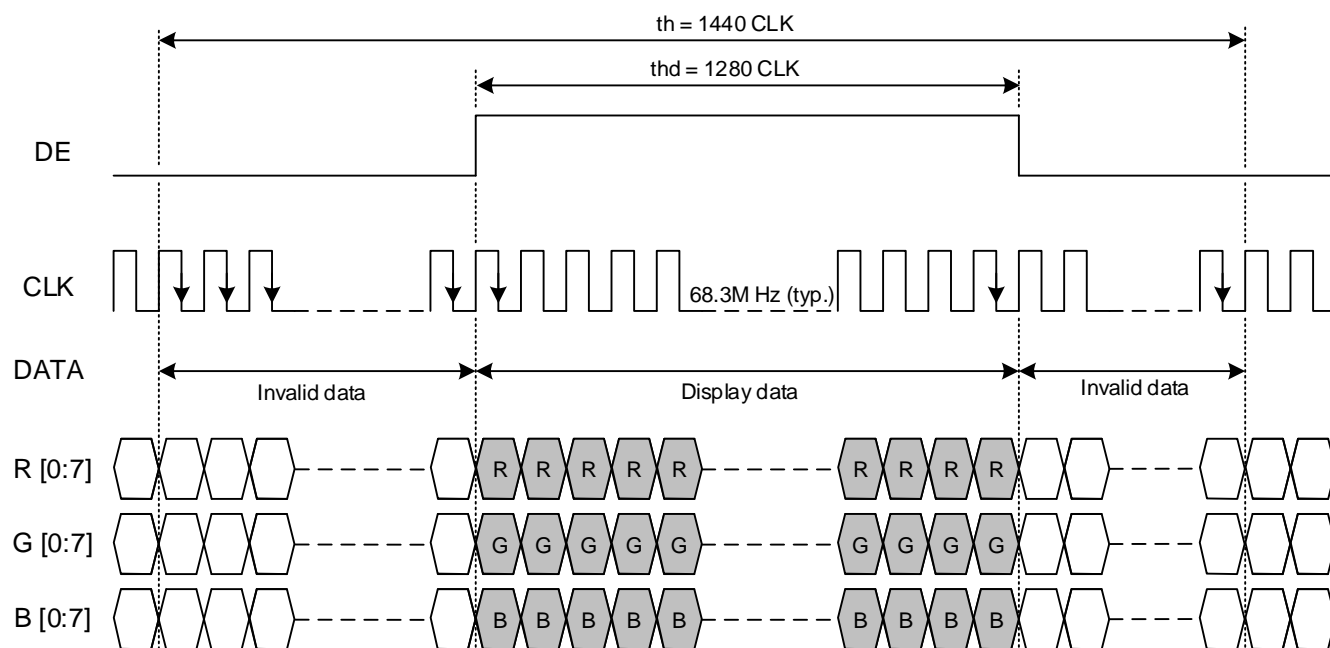


Fig. 9.1 Horizontal Timing

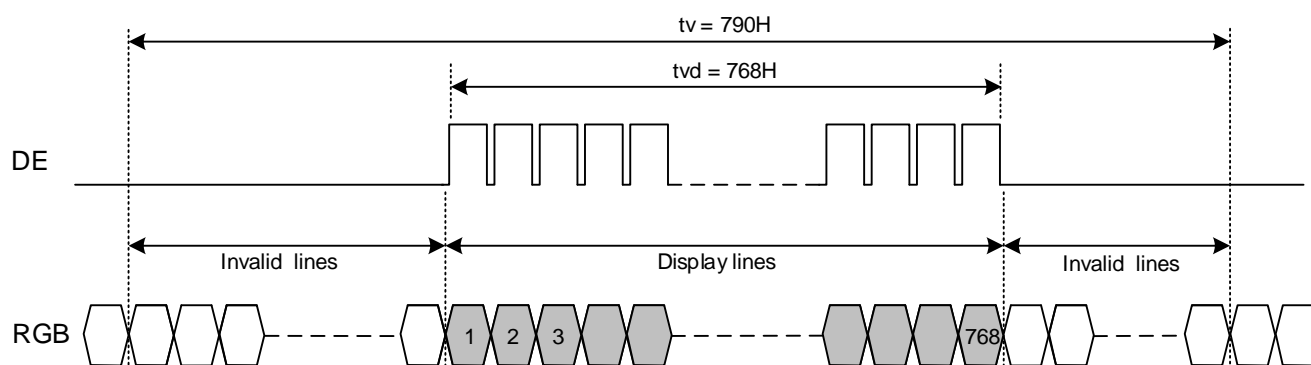


Fig. 9.2 Vertical Timing

9.5 TIME TABLE

The column of timing sets including minimum, typical, and maximum as below are based on the best optical performance, frame frequency (f_{Frame}) = 60 Hz to define. If 60 Hz is not the aim to set, less than 75 Hz for f_{Frame} is recommended to apply for better performance by other parameter combination as the definitions in section 5.1.

	Item	Symbol	Min.	Typ.	Max.	Unit
Horizontal	CLK Frequency	fclk	50	68.3	80	M Hz
	Display Data	thd	1280			CLK
	Cycle Time	th	1322	1440	-	
Vertical	Display Data	tvd	768			H
	Cycle Time	tv	778	790	-	
	Frequency	fv	55	60	75	Hz

9.6 DISPLAY MODE CONTROL

Scan direction is available to be switched as below by setting CN1's SD pin.

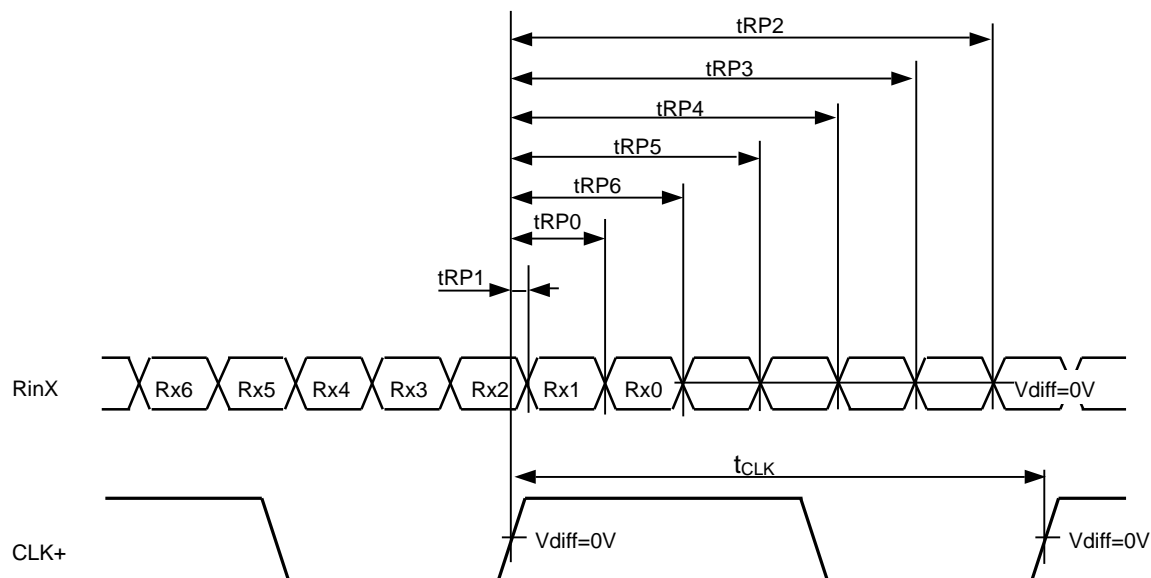


SD : Low / Default



SD : High

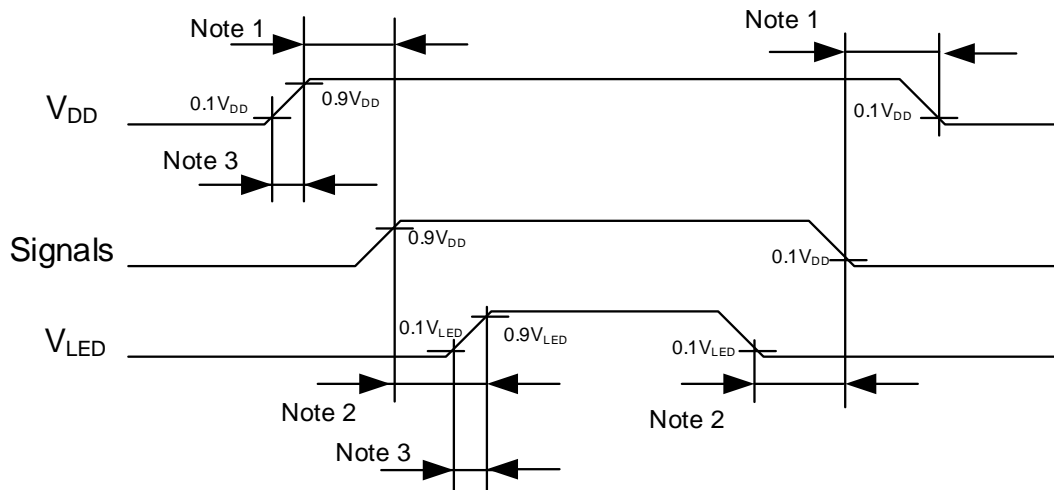
9.7 LVDS RECEIVER TIMING



$$R_{inX} = (R_{inX+}) - (R_{inX-}) \quad (X=0, 1, 2, 3)$$

	Item	Symbol	Min.	Typ.	Max.	Unit
CLK	Cycle frequency	$1/t_{CLK}$	50	68.3	80	MHz
RinX (X=0,1,2,3)	0 data position	t_{RP0}	$1/7t_{CLK}-0.4$	$1/7*t_{CLK}$	$1/7t_{CLK}+0.4$	ns
	1st data position	t_{RP1}	-0.4	0	-0.4	
	2nd data position	t_{RP2}	$6/7t_{CLK}-0.4$	$6/7*t_{CLK}$	$6/7t_{CLK}+0.4$	
	3rd data position	t_{RP3}	$5/7t_{CLK}-0.4$	$5/7*t_{CLK}$	$5/7t_{CLK}+0.4$	
	4th data position	t_{RP4}	$4/7t_{CLK}-0.4$	$4/7*t_{CLK}$	$4/7t_{CLK}+0.4$	
	5th data position	t_{RP5}	$3/7t_{CLK}-0.4$	$3/7*t_{CLK}$	$3/7t_{CLK}+0.4$	
	6th data position	t_{RP6}	$2/7t_{CLK}-0.4$	$2/7*t_{CLK}$	$2/7t_{CLK}+0.4$	

9.8 POWER SEQUENCE



Note 1: In order to avoid any damages, V_{DD} has to be applied before all other signals. The opposite is true for power off where V_{DD} has to be remained on until all other signals have been switch off. The recommended time period is 1 second.

Note 2: In order to avoid showing uncompleted patterns in transient state. It is recommended that switching the backlight on is delayed for 1 second after the signals have been applied. The opposite is true for power off where the backlight has to be switched off 1 second before the signals are removed.

Note 3: In order to avoid high Inrush current, V_{DD} rising time need to set more than 0.5ms.

9.9 DATA INPUT for DISPLAY COLOR

9.9.1 MODE = Low

Input color		Red Data						Green Data						Blue Data					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
		MSB					LSB	MSB					LSB	MSB					LSB
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Note 1: Definition of gray scale : Color(n) Number in parenthesis indicates gray scale level. Larger number corresponds to brighter level.

Note 2: Data Signal : 1 : High, 0 : Low

9.9.2 MODE = High

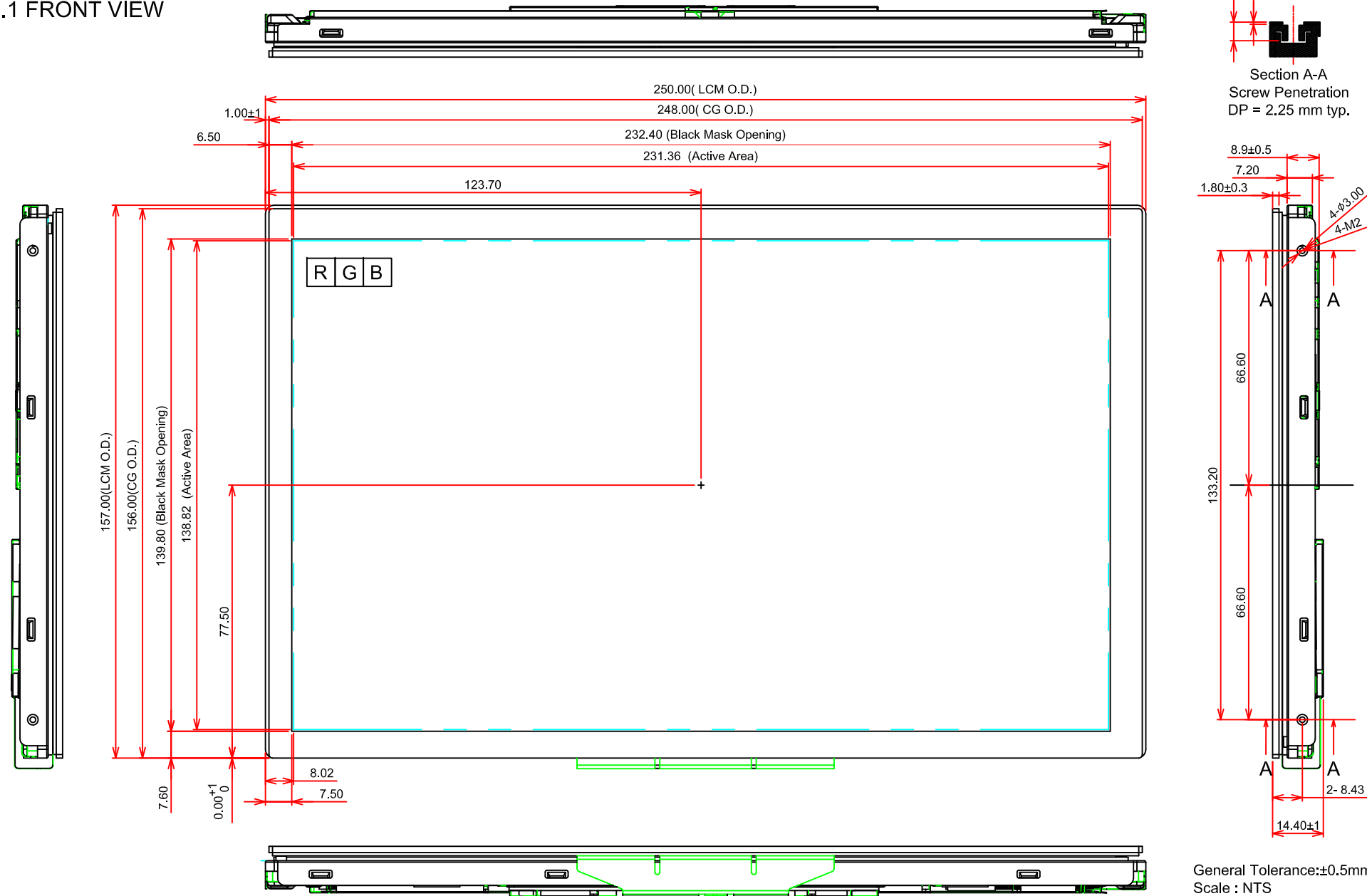
Input color		Red Data								Green Data								Blue Data							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
		MSB								LSB								MSB							
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note 1: Definition of gray scale : Color(n) Number in parenthesis indicates gray scale level. Larger number corresponds to brighter level.

Note 2: Data Signal : 1 : High, 0 : Low

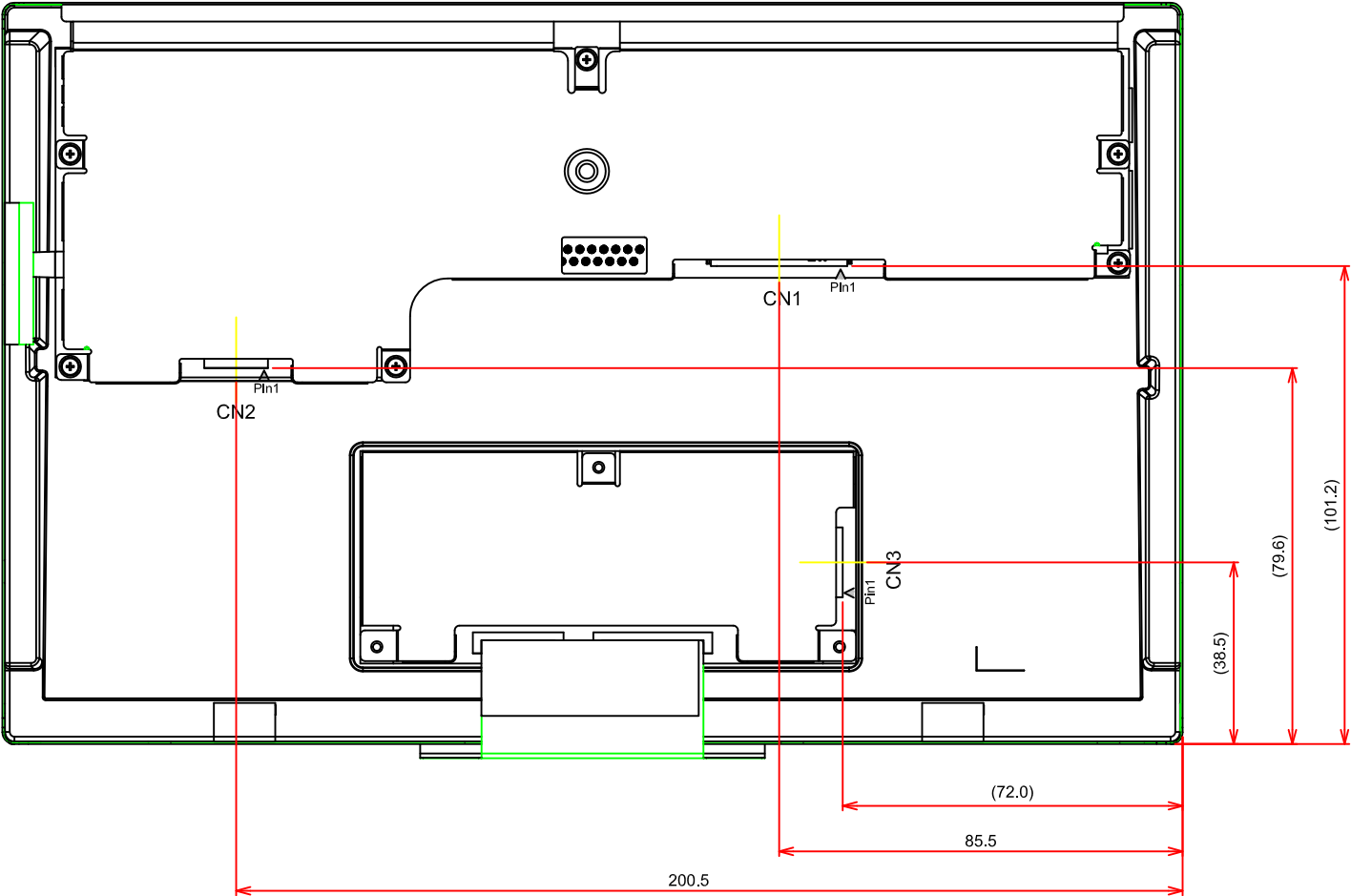
10. OUTLINE DIMENSIONS

10.1 FRONT VIEW



General Tolerance:±0.5mm
Scale : NTS
Unit : mm

10.2 REAR VIEW



General Tolerance:±0.5mm
Scale : NTS
Unit : mm

11. TOUCH PANEL

The type of touch panel used on this display is capacitive touch panel film, and more characteristics are shown as below:

11.1 MECHANICAL CHARACTERISTICS

Item	Specification	Remarks
Thickness	2.7mm	
CG Thickness	1.8 mm	Strengthened Glass
CG Material	Soda lime	-
Surface Hardness	$\geq 7H$	-
Operating Method	Fingers(main use) / Gloves	-
Touch Function	10	Finger multi touch support
Finger separation	30mm @13mm ϕ	-

11.2 ELECTRICAL CHARACTERISTICS

Item	Symbol	Value			Unit
		Min.	Typ.	Max.	
Power supply voltage	V _{DD} 3V3	-0.3	+3.3	+3.6	V
Operation current	I _{dd}	-	-	200	mA
Input high level voltage	V _{IH}	0.8V _{DD}	-	-	V
Input low level voltage	V _{IL}	-	-	0.4	V
Output high voltage	V _{OH}	V _{DD} -2mA	-	-	V
Output low voltage	V _{OL}	-	-	0.4	V

11.3 CONTROLLER CHARACTERISTICS

The Capacitive Touch Panel features as below:

- Power Supply: 5V for USB I/F
- Power Consumption: < 90mA
- Controller IC is EETI EXC80W46
- USB2.0 compliant full speed with LPM L1 supported
- OS: Windows / Linux / Android
- Firmware version : A0651E_0001_PCT-106F-03_EE80W462840-CBBC-B001_v03_ECN_20211129

12. APPEARANCE STANDARD

The appearance inspection is performed in a room around 500~1000 lx based on the conditions as below:

- The distance between inspector's eyes and display is 30 cm.
- The viewing zone is defined with angle θ shown in Fig. 12.1 The inspection should be performed within 45° when display is shut down. The inspection should be performed within 5° when display is power on.

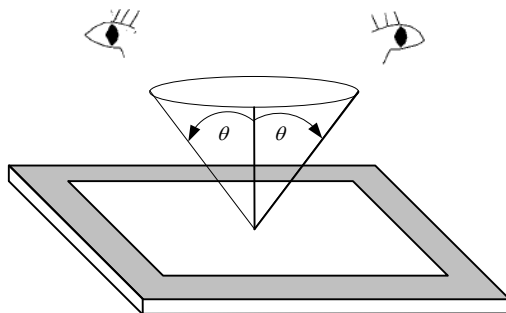


Fig. 12.1

12.1 THE DEFINITION OF LCD ZONE

LCD panel is divided into 3 areas as shown in Fig.12.2 for appearance specification in next section. A zone is the LCD active area (dot area); B zone is the area between A zone and metal frame.

In terms of housing design, B zone is the recommended window area customers' housing should be located in.

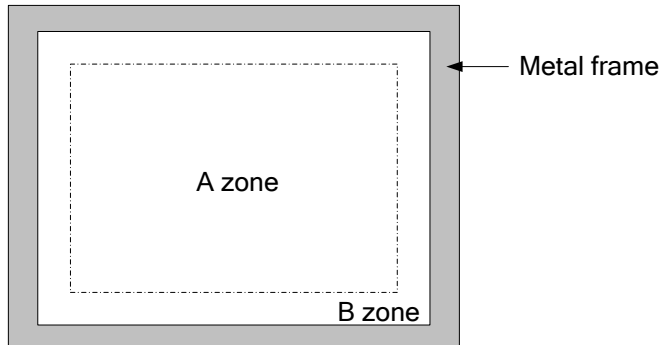


Fig. 12.2

12.2 LCD APPEARANCE SPECIFICATION

The specification as below is defined as the amount of unexpected phenomenon or material in different zones of LCD panel. The definitions of length, width and average diameter using in the table are shown in Fig. 12.3 and Fig. 12.4.

Item	Criteria				Applied zone
Scratches	Length (mm)	Width (mm)	Maximum number	Minimum space	A, B
	Ignored	$W \leq 0.02$	Ignored	-	
	$L \leq 40$	$0.02 < W \leq 0.05$	10	-	
	-	$0.05 < W$	Not allowed	-	
Dent	Serious one is not allowed				A
Wrinkles in polarizer	Serious one is not allowed				A
Bubbles on polarizer	Average diameter (mm)		Maximum number		A
	$D \leq 0.3$		Ignored		
	$0.3 < D \leq 0.5$		12		
	$0.5 < D$		Not allowed		
1) Stains 2) Foreign Materials 3) Dark Spot	Filamentous (Line shape)				A, B
	Length (mm)	Width (mm)		Maximum number	
	$L \leq 2.0$	$W \leq 0.03$		Ignored	
	$L \leq 3.0$	$0.03 < W \leq 0.05$		10	
	$L \leq 2.5$	$0.05 < W \leq 0.1$		1	
	Round (Dot shape)				A, B
	Average diameter (mm)		Maximum number	Minimum Space	
	$D \leq 0.2$		Ignored	-	
	$0.2 < D \leq 0.3$		10	10 mm	
	$0.3 < D \leq 0.4$		5	30 mm	
	$0.4 < D$		Not allowed	-	
	In total		Filamentous + Round=10		
	Those wiped out easily are acceptable				
Dot-Defect (Note 1)		Type	Maximum number		A
	Bright dot-defect	1 dot	0		
	Dark dot-defect	1 dot	5		
		2 adjacent dot	2		
		3 adjacent dot or above	Not allowed		
		In total	5		
	In total			5	

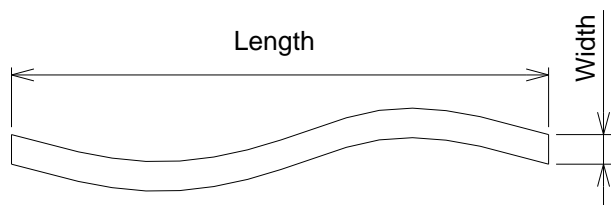


Fig 12.3

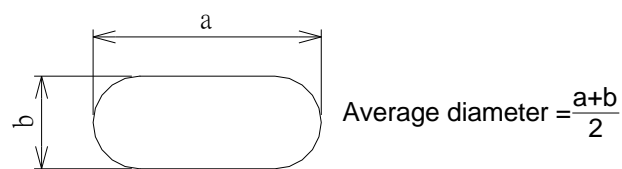


Fig 12.4

Note 1: The definitions of dot defect are as below:

- For bright dot-defect, showing black pattern, visible with defect size over 1/2 dot is defined.
- For dark dot-defect, showing white pattern, defect size over 1/2 dot area is defined.
- The definition of 1-dot-defect is the defect-dot, which is isolated and no adjacent defect-dot.
- The definition of adjacent dot is shown as Fig. 12.5.
- The Density of dot defect is defined in the area within diameter $\phi = 10\text{mm}$.

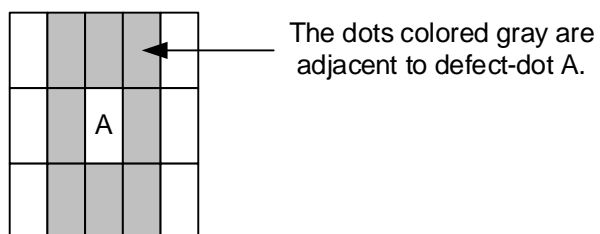


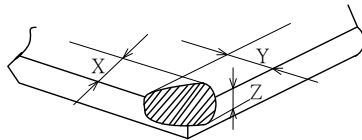
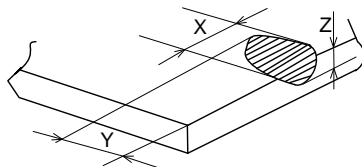
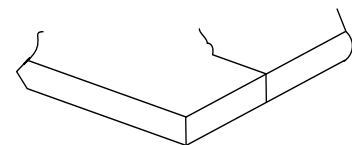
Fig. 12.5

12.3 TOUCH PANEL APPEARANCE SPECIFICATION

The specification as below is defined by the amount of unexpected material in different zones of touch panel.

Item	Criteria				Applied zone
Scratches	Width (mm)	Length (mm)	Maximum number	Minimum Space	A
	$W \leq 0.1$	$L \leq 5$	Ignored	-	
	$0.1 < W \leq 0.15$	$L \leq 5$	5	5mm	
	$0.15 < W$	-	0		
Foreign Materials	Filamentous (Line shape)				A
	Width (mm)	Length (mm)	Maximum number		
	$W \leq 0.1$	$L \leq 4$	Ignored		
	$0.1 < W \leq 0.15$	$L \leq 5$	3	5mm	
	$0.15 < W$	-	0		
	Round (Dot shape)				
	Average diameter (mm)		Maximum number		
	$D \leq 0.2$		Ignored		
	$0.2 < D \leq 0.5$		5	5mm	
	$0.5 < D$		0		
Pin Hole	$D \leq 0.2$		Ignored		A
	$0.2 < D$		0		

The limitation of glass flaw occurred on touch panel is defined in the table as below.

Item	Specifications		Maximum number
Corner flaw		$X \leq 0.3\text{mm}$	Ignored
		$Y \leq 0.3\text{mm}$	
		$Z \leq 1/2T$	
		$0.3\text{mm} < X \leq 0.5\text{mm}$	$N \leq 3$
		$0.3\text{mm} < Y \leq 0.5\text{mm}$	
		$Z \leq 1/2T$	
Edge flaw		$X \leq 0.3\text{mm}$	Ignored
		$Y \leq 0.3\text{mm}$	
		$Z \leq 1/2T$	
		$0.3\text{mm} < X \leq 0.5\text{mm}$	$N \leq 1$
		$0.3\text{mm} < Y \leq 0.5\text{mm}$	
		$Z \leq 1/2T$	
Progressive flaw		Not allowed	

13. PRECAUTIONS

13.1 PRECAUTIONS of ESD

- 1) Before handling the display, please ensure your body has been connected to ground to avoid any damages by ESD. Also, do not touch display's interface directly when assembling.
- 2) Please remove the protection film very slowly before turning on the display to avoid generating ESD.

13.2 PRECAUTIONS of HANDLING

- 1) In order to keep the appearance of display in good condition, please do not rub any surfaces of the displays by sharp tools harder than 3H, especially touch panel, metal frame and polarizer.
- 2) Please do not pile the displays in order to avoid any scars leaving on the display. In order to avoid any injuries, please pay more attention for the edges of glasses and metal frame, and wear finger cots to protect yourself and the display before working on it.
- 3) Touching the display area or the terminal pins with bare hand is prohibited. This is because it will stain the display area and cause poor insulation between terminal pins, and might affect display's electrical characteristics furthermore.
- 4) Do not use any harmful chemicals such as acetone, toluene, and isopropyl alcohol to clean display's surfaces.
- 5) Please use soft cloth or absorbent cotton with ethanol to clean the display by gently wiping. Moreover, when wiping the display, please wipe it by horizontal or vertical direction instead of circling to prevent leaving scars on the display's surface, especially polarizer.
- 6) Please wipe any unknown liquids immediately such as saliva, water or dew on the display to avoid color fading or any permanently damages.
- 7) Maximum pressure to the surface of the display must be less than 1.96×10^4 Pa. If the area of adding pressure is less than 1 cm^2 , the maximum pressure must be less than 1.96N.

13.3 PRECAUTIONS OF OPERATING

- 1) Please input signals and voltages to the displays according to the values defined in the section of electrical characteristics to obtain the best performance. Any voltages over than absolute maximum rating will cause permanent damages to this display. Also, any timing of the signals out of this specification would cause unexpected performance.
- 2) When the display is operating at significant low temperature, the response time will be slower than it at 25°C . In high temperature, the color will be slightly dark and blue compared to original pattern. However, these are temperature-related phenomenon of LCD and it will not cause permanent damages to the display when used within the operating temperature.
- 3) The use of screen saver or sleep mode is recommended when static images are likely for long periods of time. This is to avoid the possibility of image sticking.
- 4) Spike noise can cause malfunction of the circuit. The recommended limitation of spike noise is no bigger than $\pm 100 \text{ mV}$.

JDI Taiwan Inc. Kaohsiung Branch	SHEET NO.	7B64PS 2713-TX27D200VM0AVB-5	PAGE	13-1/2
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12.4 PRECAUTIONS of STORAGE

If the displays are going to be stored for years, please be aware the following notices.

- 1) Please store the displays in a dark room to avoid any damages from sunlight and other sources of UV light.
- 2) The recommended long term storage temperature is between 10C° ~35C° and 55%~75% humidity to avoid causing bubbles between polarizer and LCD glasses, and polarizer peeling from LCD glasses.
- 3) It would be better to keep the displays in the container, which is shipped from JDI, and do not unpack it.
- 4) Please do not stick any labels on the display surface for a long time, especially on the polarizer.

14. DESIGNATION of LOT MARK

1) The lot mark is showing in Fig.14.1. First 4 digits are used to represent production lot, T represented product of JDI Taiwan, and the last 6 digits are the serial number.

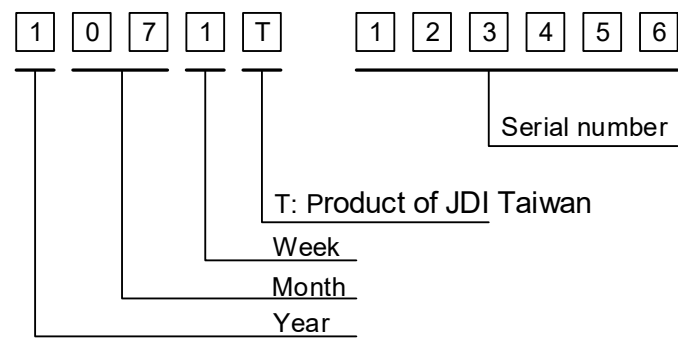


Fig. 14.1

2) The tables as below are showing what the first 4 digits of lot mark are shorted for.

Year	Lot Mark	Month	Lot Mark	Month	Lot Mark	Week	Lot Mark
2021	1	Jan.	01	Jul.	07	1~7 days	1
2022	2	Feb.	02	Aug.	08	8~14 days	2
2023	3	Mar.	03	Sep.	09	15~21 days	3
2024	4	Apr.	04	Oct.	10	22~28 days	4
2025	5	May	05	Nov.	11	29~31 days	5
		Jun.	06	Dec.	12		

3) Except letters I and O, revision number will be shown on lot mark and following letters A to Z.

REV. No	ITEM	REMARKS
A	-	-
B	LCD source change	PCN 1110

4) The location of the lot mark is on the back of the display shown in Fig. 14.2

Label example :



Fig. 14.2